

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		11077696	
	Filing Date		2005-03-10	
	First Named Inventor		SHAH	
	Art Unit			
	Examiner Name			
	Attorney Docket Number		020103	

U.S.PATENTS						
Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1	6118811		2000-09-12	NARUMI	

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	1	0473373	EP		1994-04-05	ROCKWELL INTERNATIONAL CORPORATION (LOPER)		<input type="checkbox"/>
	2	1067675	EP		2005-04-27	SONY CORP		<input type="checkbox"/>
	3	2000-515342	JP		2000-01-11	HEWLETT-PACKARD		<input type="checkbox"/>

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	4	9950966	WO		1999-10-07	TELEFONAKTIEBOLAG ET LM ERICSSON		<input type="checkbox"/>
	5	9804050	WO		2007-12-14	ERICSSON INC		<input type="checkbox"/>

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NON-PATENT LITERATURE DOCUMENTS

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	1	GRAY AND MEYER, "ANALYSIS AND DESIGN OF ANALOG INTEGRATED CIRCUITS" 1984, Wiley, US XP002245777 p 593 - 598	<input type="checkbox"/>
	2	ISR-EP-1470650_04/18/07	<input type="checkbox"/>
	3	ISR-EP-1531553_05/18/05	<input type="checkbox"/>
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